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IN THE U.S. PATENT & TRADEMARK OFFICE

First Named Inventor: Jonathan Yang Chen :

GROUP ART UNIT 2115

Pat Appln: Serial No. 09/824,389:

Examiner: Pranav Chandrasekhar

Filed: April 2, 2001 :

August 5, 2004

5 Title: Method and system of automatic delay detection and receiver adjustment for
synchronous bus interface

Attorney Docket POU920000073US1 :

Deposit Account: 09-0463 :

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AMENDMENT (Responsive to OA Dated 5/14/04)

Commissioner for Patents


P.O. Box 1450

15 Alexandria, VA 22313-1450

Sir:

A. Introductory Comments:

In reply to the Official action mailed 5/14/04 please enter
this amendment. Thereafter, Applicants respectfully request
20 review of the following amendment made to place the application
in condition for allowance after reexamination of the present
application in light of these amendments. (The Commissioner is
also hereby authorized to charge payment of the fees associated
with this communication or credit any overpayment to Deposit
25 Account No. 09-0463. Those expected are already part of the
separate fee authorization.)

CERTIFICATE OF MAILING UNDER 37 C.F.R. 1.8(a)	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on August 5, 2004.	
 Susan L. Nelson	8/5/04 Date of Signature